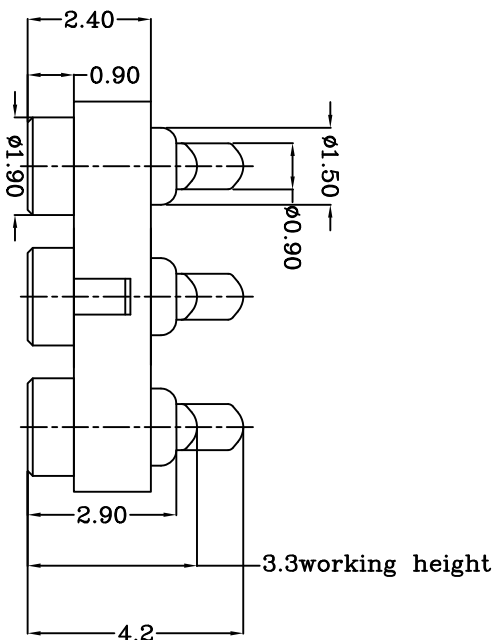
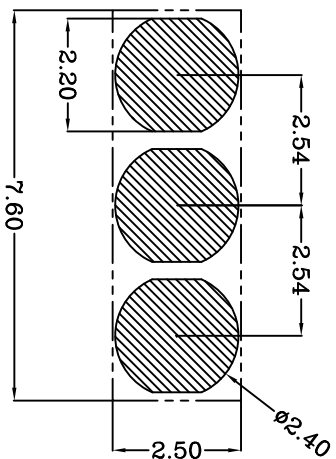


DWG NO.
C-110341



TOP-LINK



Recommended PCB Layout

Plating Body: 0.1µm Au over 1.4µm Ni.
Plating Plunger: 0.4µm Au over 1.4µm Ni

DIMENSIONS IN MM TOLERANCES UNLESS OTHER SPECIFIED				MATL 		HT TR —		FIN — 	
0	= ±		DVN	Qln	APVD	<i>teagyr-Litnik</i> <u>Electronics</u> Top-Link Electronics Ltd.			
0.0	= ±0.15				Lansing				
0.00	= ±0.10		CHK	Jlm	REL				
0.000	= ±								
ANGLE: ±1°									
SURF TEXTURE: 3.2/ 		NAME BATTERY PGGD CONNECTOR 3 POSITIONS							
REMOVE BURRS, BREAK SHARP EDGES R0.05 MAX DO NOT SCALE PRINT		SCALE 1:1 A4 DWG NO. C-110341 SHEET 1 OF 1 REV A							
		PGC-1.5-4.2-R-3P-2.54PH							

110341-3
PART NUMBER

USED ON

1